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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/827,993	04/05/2001	Roland L. Fernandez	MS 154593.2/40062.108USU1	5192
27488	7590	08/25/2004	EXAMINER PILLAI, NAMITHA	
MICROSOFT CORPORATION C/O MERCHANT & GOULD, L.L.C. P.O. BOX 2903 MINNEAPOLIS, MN 55402-0903			ART UNIT 2173	PAPER NUMBER

DATE MAILED: 08/25/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Sc

Office Action Summary	Application No. 09/827,993	Applicant(s) FERNANDEZ ET AL.	
	Examiner Namitha Pillai	Art Unit 2173	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
 - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
 - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
 - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 22 April 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-15, 17-28 and 30-33 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-15, 17-28 and 30-33 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 05 April 2001 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Drawings

1. The drawings are objected to as failing to comply with 37 CFR 1.84(p)(5) because they do not include the following reference sign(s) mentioned in the description: Figure 1 does not have a reference number 134. Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Specification

2. The disclosure is objected to because it contains an embedded hyperlink and/or other form of browser-executable code. Applicant is required to delete the embedded hyperlink and/or other form of browser-executable code. See MPEP § 608.01.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. Claims 1-15, 17-28 and 30-33 are rejected under 35 U.S.C. 102(e) as being clearly anticipated by U. S. Patent No. 6, 668, 354 B1 (Chen et al.), herein referred to as Chen.

Referring to claim 1, Chen discloses a computer system configured for providing themes for graphical components, the computer system having memory (column 1, lines 60-67). Chen also discloses a selecting module receiving a user request for a selected theme having an associated non-binary theme file with theme properties (column 1, lines 36-39). Chen discloses a converting module converting the associated non-binary theme file into a binary theme file to facilitate retrieval of theme properties (column 3, lines 25-41). Chen also discloses a loading module loading the binary theme file into the memory so that themes can be applied to the graphical components (column 8, lines 45-55).

Referring to claim 2, Chen discloses a plurality of processes, each process accessing the binary theme file (column 8, lines 45-48).

Referring to claim 3, Chen discloses an update handle module receiving a theme handle request from a graphical component and distributing a theme handle if the graphical component is found in the binary theme file so that the graphical component can use the theme properties of the binary theme file and a close handle module closing the theme handle and decrementing a reference count on the shared memory in response to process termination so that a theme handle can be closed when a binary theme file is loaded (Figure 7A).

Referring to claim 4, Chen discloses a notification module notifying the processes that a new theme file has been loaded (column 3, lines 37-41).

Referring to claim 5, Chen discloses a schema file parsing module parsing a schema file containing a list of all themeable graphical components and properties (column 3, lines 30-35).

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Chen also discloses a theme specification file parsing module parsing a theme specification file specifying graphical component sizes and colors (column 3, lines 30-35). Chen also discloses a building module building a binary theme file containing the graphical components, properties, sizes, and colors in a binary format (column 7, lines 30-40).

Referring to claim 6, Chen discloses that binary format is hierarchical, there being a data section for each hierarchy, the sections being a global section, a class section, a parts section, and a states section (column 8, lines 27-36).

Referring to claim 7, Chen discloses converting module further builds a packed data object section having all the theme properties for a class, part, and state (column 8, lines 37-38).

Referring to claims 8 and 21, Chen discloses a method for creating a visual style for a set of graphical components for use on a computer system having a graphical operating environment and processes with shared memory (column 1, lines 60-67). Chen discloses selecting graphical components from a schema file of graphical components, that are desired to have a defined visual style, each component being defined by a unique class name (column 4, lines 4-9). Chen also discloses assigning properties to the selected components according to the defined visual style so that each selected component has assigned properties (column 4, lines 9-10). Chen also discloses grouping the pairs of selected graphical components and corresponding assigned properties for the defined visual style together in a class data file, converting the class data file into a binary theme file having a class data section having class names and assigned properties in a binary format and loading the binary theme file into the shared memory so that a visual style can be used to render graphical components (column 4, lines 5-25).

Referring to claims 9 and 22, Chen discloses that the graphical components defined within the schema file of graphical components have one or more part names associated with at least one class name, and the converting act further comprises creating a part property data section in the binary theme file, the part property data section having the one or more part names and the assigned properties (column 4, lines 4-13).

Referring to claims 10 and 23, Chen discloses that the graphical components defined within the schema file of graphical components have one or more state names associated with at least one defined part name, and the converting act further comprises creating a state property data section in the binary theme file, the state property data section having the one or more state names and the assigned properties (column 4, lines 4-13).

Referring to claims 11 and 24, Chen discloses identifying some properties as global properties, creating in the binary theme file a global properties section having the global properties to be used when a class name, part name, or state name cannot be found in the binary theme file (column 4, lines 4-13).

Referring to claims 12 and 25, Chen discloses a list of available properties is within the first schema file of graphical components, that may be selected in the selecting step for each graphical component, part and state (column 4, lines 4-13).

Referring to claims 13 and 26, Chen discloses identifying a derived property for a graphical component and associating a unique numeric identifier with the derived property to create a derived property identifier (column 3, lines 32-34). Chen also discloses identifying one or more primitive properties for each derived property, wherein each primitive property has associated property data having a length, associating a unique numeric identifier with each

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primitive property, to create a primitive property identifier, calculating the lengths of each of the associated property data, selecting a derived property identifier, writing a binary tagged data module to a tagged data memory offset in the class data section of the binary file wherein the binary tagged data module contains the selected derived property identifier, the one or more, primitive property identifiers, the associated property values, and each of the property values' lengths and writing an associated parent part offset after each binary tagged data module, the associated parent part offset being a memory offset into the global class section (Figure 7A and column 6, lines 30-60).

Referring to claims 14 and 27, Chen discloses obtaining the memory offset of a binary tagged data module for a state and writing the memory offset to a second memory offset in a state jump table in the binary theme file (column 6, lines 35-50).

Referring to claims 15 and 28, Chen discloses writing the second memory offset to a third memory offset in a part jump table in the binary theme file (column 4, lines 38-47).

Referring to claims 17 and 30, Chen discloses a method of retrieving graphical component theme property data on a computer system having a graphical operating system and processes (column 1, lines 8-11). Chen discloses receiving a rendering request from a graphical component, the request having a theme handle and a component state accessing a binary theme file to retrieve theme property data for the requesting process (Figure 7A) and retrieving graphical component theme property data from the binary theme file (column 6, lines 60-65).

Referring to claims 18 and 31, Chen discloses retrieving an offset into a class data section of the binary theme file, the class data section having theme property data for a class in binary format (column 6, lines 40-45), performing a binary search for class property data at the

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offset determining if class property data exists at the offset and jumping to a global data section of the binary theme file having global theme property data, if no class property data is found and retrieving global theme property data from the global data section (column 7, lines 11-17).

Referring to claims 19 and 32, Chen discloses retrieving an offset into a part jump table section of the binary theme file, the part jump table section having theme property data for a part in binary format performing a binary search for part property data at the offset determining if part property data exists at the offset, jumping to a class data section of the binary theme file having theme property data for a class, if no part property data is found and retrieving class theme property data from the class data section (column 7, lines 11-17).

Referring to claims 20 and 33, Chen discloses retrieving a memory offset into a part jump table section of the binary theme file, retrieving from the part jump table section a second memory offset into a state jump table section and jumping to the second memory offset of the binary theme file having state theme property data and retrieving state theme property data from the state theme property data section (column 6, lines 38-50).

Conclusion

4. The prior art made of record on form PTO-892 and not relied upon is considered pertinent to applicant's disclosure. Applicant is required under 37 C.F.R. § 1.111(c) to consider these references fully when responding to this action. The documents cited therein teach the method for displaying web pages in a 3D environment.

Responses to this action should be mailed to: Commissioner of Patents and Trademarks, Washington D.C. 20231. If applicant desires to fax a response, central FAX number (703) 872-9306 may be used. NOTE: A Request for Continuation (Rule 60 or 62) cannot be faxed.

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Please label "PROPOSED" or "DRAFT" for informal facsimile communications. For after final responses, please label "AFTER FINAL" or "EXPEDITED PROCEDURE" on the document. Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA., Sixth Floor (Receptionist).

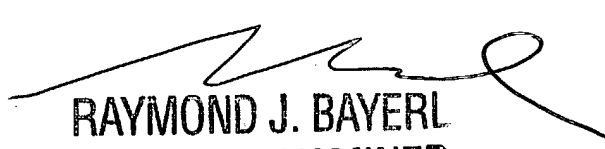
Any inquiry concerning this communication or earlier communications from the examiner should be directed to Namitha Pillai whose telephone number is (703) 305-7691. The examiner can normally be reached on 8:30 AM - 5:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Cabeca can be reached on (703) 308-3116.

All Internet e-mail communications will be made of record in the application file. PTO employees do not engage in Internet communications where there exists a possibility that sensitive information could be identified or exchanged unless the record includes a properly signed express waiver of the confidentiality requirements of 35 U.S.C. 122. This is more clearly set forth in the Interim Internet Usage Policy published in the Official Gazette of the Patent and Trademark on February 25, 1997 at 1195 OG 89.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 305-3800.

Namitha Pillai
Assistant Examiner
Art Unit 2173
August 19, 2004



RAYMOND J. BAYERL
PRIMARY EXAMINER
ART UNIT 2173